Journal of
Low Power Electronics and Applications

an Open Access Journal by MDPI

Editor-in-Chief
Prof. Pasquale Corsonello
Department of Informatics,
Modeling, Electronics and
System Engineering, University
of Calabria, Rende (CS) 87036,
Italy

pasquale.corsonello@unical.it

Message from the Editor-in-Chief

Journal of Low Power Electronics and Applications is an open access journal which provides an advanced forum for rapid dissemination of innovative research and important results in all aspects of low power electronics and design.

It publishes reviews, regular research papers and short communications. Our aim is to encourage scientists to publish their experimental and theoretical results in as much detail as possible. The full experimental details must be provided so that the results can be reproduced.

Author Benefits

- **Open Access** Unlimited and free access for readers
- **No Copyright Constraints** Retain copyright of your work and free use of your article
- **Rapid Publication** The first decision provided to authors approximately 16 days after submission; acceptance to publication is undertaken in 4.5 days (median values for papers published in the first six months of 2020)
- **Thorough Peer-Review**
- **High Visibility** covered by Inspec (IET) and Scopus
- **No Space Constraints, No Extra Space or Color Charges** No restriction on the length of the papers, number of figures or colors
- **Discounts on Article Processing Charges (APC)** If you belong to an institute that participates with the MDPI Institutional Open Access Progra

Aims and Scope

1. Devices and Technologies

   Advanced technology nodes for low-power; emerging nanomaterials and nanotechnologies; sensors; energy storage devices.

2. Circuits

   Low-power digital circuits for logic; low-power analog/mixed-signal circuits; ultra-low-power RF circuits; ultra-low-power boost converter.

3. Architectures

   Low-power microarchitecture design; asynchronous design; System-on-Chip designs; approximate, brain-inspired and other non-conventional computing; HW/SW co-design; low-power architecture for image, video and graphic processing; SoC FPGA for low-power.

4. CAD tools and methodologies

   CAD tools and methodologies for low-power and thermal-aware design; power-aware synthesis and optimization; dynamic power management; energy-efficient software design and software for low-power applications in heterogeneous systems.

5. Systems and applications

   Wearable computing; circuits and systems for Internet-of-Things (IoT); deep-learning low-power systems; implantable electronics; bio-sensor circuits; battery-less applications.

MDPI is a member of

Follow Us

f facebook.com/MDPIOpenAccessPublishing

🐦 twitter.com/MDPIOpenAccess

in linkedin.com/company/mdpi

🕸 weibo.com/mdpicn

💬 Wechat: MDPI-China

Ⓦ blog.mdpi.com

www.mdpi.com   **mdpi.com/journal/jlpea**