



First International Workshop on  
**Network on Chip Architectures**

To be held in conjunction with the  
41st Annual IEEE/ACM International Symposium on Microarchitecture (MICRO-41)  
8<sup>th</sup> November, 2008  
Lake Como, Italy

## Preliminary Program

Time	Event
<b>13.30 – 14.20</b>	<b>Opening Session</b>
13.30 – 13.35	<b>Welcome from the Organizers</b> Maurizio Palesi and Shashi Kumar - <i>University of Catania, Italy and Jönköping University, Sweden</i>
13.35 – 14.15	<b>Keynote talk: Managing Heterogeneity in Future NoCs</b> <i>José Duato, UPV Valencia, Spain</i>
<b>14.20 – 15.00</b>	<b>Session I ♦ Router Microarchitecture</b>
14.20 – 14.40	<b>Planar Adaptive Router Microarchitecture for Tree-Based Multicast Network-on-Chip</b> Faizal Arya Samman, Thomas Hollstein and Manfred Glesner - <i>TU Darmstadt, Germany</i>
14.40 – 15.00	<b>DMesh: a Diagonally-Linked Mesh Network-on-Chip Architecture</b> Wen-Hsiang Hu, Seung Eun Lee, and Nader Bagherzadeh - <i>University of California, Irvine, USA</i>
<b>15.00 – 15.30</b>	<b>Coffee Break</b>
<b>15.30 – 16.30</b>	<b>Session II ♦ Performance Evaluation</b>
15.30 – 15.50	<b>A Generic Traffic Model for On-Chip Interconnection Networks</b> Jun Ho Bahn and Nader Bagherzadeh - <i>Qualcomm Inc., USA and UC Irvine, USA</i>
15.50 – 16.10	<b>A System-C based Microarchitectural Exploration Framework for Latency, Power and Performance Trade-offs of On-Chip Interconnection Networks</b> Basavaraj Talwar and Bharadwaj Amrutur - <i>ECE, Indian Institute of Science, Bangalore, India</i>
16.10 – 16.30	<b>Application Specific Buffer Allocation for Wormhole Routing Networks-on-Chip</b> Liwei Wang, Yang Cao, Xiaohui Li and Xiaohu Zhu - <i>EIS, Wuhan University, China</i>
<b>16.30 – 16.40</b>	<b>Break</b>
<b>16.40 – 17.40</b>	<b>Session III ♦ Prospective Architectural Proposals</b>
16.40 – 17.00	<b>Scalable CMOS-compatible photonic routing topologies for versatile networks on chip</b> Alberto Scandurra and Ian O'Connor - <i>STMicroelectronics, Italy and Lyon Institute of Nanotechnology, France</i>
17.00 – 17.20	<b>Move Logic Not Data: A Conceptual Presentation</b> Ahmed Hemani and Muhammad Ali Shami - <i>Royal Institute of Technology, KTH, Sweden</i>
17.20 – 17.40	<b>Hierarchical Agent Architecture for Scalable NoC Design with Online Monitoring Services</b> Alexander Wei Yin, Liang Guang, Pasi Liljeberg, Pekka Rantala, Ethiopia Nigussie, Jouni Isoaho and Hannu Tenhunen - <i>University of Turku, Finland</i>